

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	30	(processor or cpu or microprocessor or micro-processor or micro processor) with (task or job or process or command) with (access near6 memory) with (semaphore or flag or lock or bit or indicator) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:35
L2	32	(processor or cpu or microprocessor or micro-processor or micro processor) same (task or job) same (access near6 memory) same (semaphore or flag or bit or indicator) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:35
L3	53	(processor or cpu or microprocessor or micro-processor or micro processor) same (task or job) same (semaphore or flag or bit or indicator) same ((first near3 memory) or (primary near3 memory)) same ((second near3 memory) or (secondary near3 memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:26
L4	35	(processor or cpu or microprocessor or micro-processor or micro processor) near8 (access near6 (task or job or process or command or instruction or function)) near8 (semaphore or flag or lock) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:28
L22	30	(processor or cpu or microprocessor or micro-processor or micro processor) with (task or job or process or command) with (access near6 memory) with (semaphore or flag or lock or bit or indicator) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:53
L23	32	(processor or cpu or microprocessor or micro-processor or micro processor) same (task or job) same (access near6 memory) same (semaphore or flag or bit or indicator) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30

L25	53	(processor or cpu or microprocessor or micro-processor or micro processor) same (task or job) same (semaphore or flag or bit or indicator) same ((first near3 memory) or (primary near3 memory)) same ((second near3 memory) or (secondary near3 memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L36	35	(processor or cpu or microprocessor or micro-processor or micro processor) near8 (access near6 (task or job or process or command or instruction or function)) near8 (semaphore or flag or lock) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L40	16	(processor near6 (semaphore or flag or lock) near6 (task or job or command or instruction) near6 access) with (block\$2 or prevent\$2 or restrict\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:35
L41	20	(processor near6 (semaphore or flag or lock or bit) near6 (task or job or command or instruction) near6 access) with (block\$2 or prevent\$2 or restrict\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:35
L42	21	(processor near6 (semaphore or flag or lock or bit or indicator) near6 (task or job or command or instruction) near6 access) with (block\$2 or prevent\$2 or restrict\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L43	2	"5392433".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L48	15	(programmable logic device with configur\$3 with (advantag\$4 or benef\$5))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L50	39	((each near2 (processor or cpu or microprocessor)) or (every near2 (processor or cpu or microprocessor))) near4 (memory) with program	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30

L56	86	idle processor with (task or job)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:35
L57	9	number near6 idle processor with (task or job)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L58	45	identifier near3 (task or job) near3 type	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:41
L59	8	identifier near3 (task or job) type	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L61	23	every (processor or cpu or microprocessor) with (memory or storage or ?RAM) with (attach\$2 or coupled or connected)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L65	23	identifier near3 (task near3 type)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L66	13	indicator near3 (task near3 type)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L67	2	"6243793".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30

L70	3	(processor or cpu or microprocessor) near4 shar\$3 memory near4 stor\$3 (program or application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L71	29	(processor or cpu or microprocessor) near4 shar\$3 memory near4 stor\$3 near4 (program or application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L72	9	(processor or cpu or microprocessor) near4 shar\$3 memory and external memory near4 stor\$3 near4 (program or application)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:30
L73	2	09/750637	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:48
L78	1	vartti.in. and multi-processor.ti.	USPAT	ADJ	ON	2005/12/19 14:52
L79	2057	711/147.ccls. or 711/151.ccls. or 711/152.ccls.	US-PGPUB; USPAT	ADJ	ON	2005/12/19 14:52
L80	1731	(processor or cpu or microprocessor or micro-processor or micro processor) with (semaphore or flag or lock or bit or indicator) same ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:54
L81	71	80 and 79	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:54
L82	760	(processor or cpu or microprocessor or micro-processor or micro processor) with (semaphore or flag or lock or bit or indicator) with ((block\$3 or restrict\$2 or prevent\$3) near6 access)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:54
L83	39	79 and 82	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/12/19 14:54